(Formerly West Bengal University of Technology) Syllabus for B. Tech in Computer Science & Engineering (Applicable from the academic session 2018-2019)

PRACTICAL

Subject Code : ECS 391	Category: Engineering Science course
Subject Name : Analog and Digital Electronics Lab	Semester : Third
L-T-P : 0-0-4	Credit:2
Pre-Requisites: No-prerequisite	

ANALOG: At least any two of the following

- 1. Design a Class A amplifier
- 2. Design a Phase-Shift Oscillator
- 3. Design of a Schmitt Trigger using 555 timer.

DIGITAL : At least any five of the following

1. Design a Full Adder using basic gates and verify its output / Design a Full Subtractor circuit using basic gates

and verify its output.

2. Construction of simple Decoder & Multiplexer circuits using logic gates.

3. Realization of RS / JK / D flip flops using logic gates.

4. Design of Shift Register using J-K / D Flip Flop.

5. Realization of Synchronous Up/Down counter.

6. Design of MOD- N Counter

7. Study of DAC.

Any one experiment specially designed by the college.

(Detailed instructions for Laboratory Manual to follow for further guidance. The details will be uploaded in the website from time to time)