

Maulana Abul Kalam Azad University of Technology, West Bengal
(Formerly West Bengal University of Technology)
Syllabus for B. Tech in Electronics & Communication Engineering
(Applicable from the academic session 2018-2019)

EC502	Computer Architecture	3L:0T:0P	3 credits
--------------	------------------------------	-----------------	------------------

Basic Structure of Computers, Functional units, software, performance issues software, machine instructions and programs, Types of instructions, Instruction sets: Instruction formats, Assembly language, Stacks, Ques, Subroutines.

Processor organization, Information representation, number formats.

Multiplication & division, ALU design, Floating Point arithmetic, IEEE 754 floating point formats

Control Design, Instruction sequencing, Interpretation, Hard wired control - Design methods, and CPU control unit. Microprogrammed Control - Basic concepts, minimizing microinstruction size, multiplier control unit. Microprogrammed computers - CPU control unit

Memory organization, device characteristics, RAM, ROM, Memory management, Concept of Cache & associative memories, Virtual memory.

System organization, Input - Output systems, Interrupt, DMA, Standard I/O interfaces

Concept of parallel processing, Pipelining, Forms of parallel processing, interconnect network

Text/Reference Books:

1. V.Carl Hammacher, "Computer Organisation", Fifth Edition.
2. A.S.Tanenbum, "Structured Computer Organisation", PHI, Third edition
3. Y.Chu, "Computer Organization and Microprogramming", II, Englewood Chiffs, N.J.,
Prentice Hall Edition
4. M.M.Mano, "Computer System Architecture", Edition
5. C.W.Gear, "Computer Organization and Programming", McGraw Hill, N.V. Edition
6. Hayes J.P, "Computer Architecture and Organization", PHI, Second edition

Course Outcomes

At the end of this course students will demonstrate the ability to

1. learn how computers work
2. know basic principles of computer's working
3. analyze the performance of computers
4. know how computers are designed and built
5. Understand issues affecting modern processors (caches, pipelines etc.).